	Application No.	Applicant(s)
Notice of Allowability	10/788,549	DO, CHANG-HO
	Examiner	Art Unit
	My-Trang N. Ton	2816
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIG	ars on the cover sheet with the co (OR REMAINS) CLOSED in this applied or other appropriate communication GHTS. This application is subject to	olication. If not included will be mailed in due course. THIS
1. This communication is responsive to <u>Admt. filed 5/26/06</u> .		
2. The allowed claim(s) is/are 13-19.		
 3.	been received. been received in Application No cuments have been received in this communication to file a reply ENT of this application. tted. Note the attached EXAMINER's reason(s) why the oath or declarate to be submitted. on's Patent Drawing Review (PTO- Amendment / Comment or in the Comment of the drawing replacement of the drawing	complying with the requirements S AMENDMENT or NOTICE OF tion is deficient. 948) attached office action of the back) of d). nust be submitted. Note the
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/06 Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Dat 8), 7. ☐ Examiner's Amendn	e

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Reasons for allowance

The following is an examiner's statement of reasons for allowance:

The present invention is drawn to a power initialization circuit utilizing a power supply voltage level follower unit, a power supply voltage detection unit and a reset prevention unit recited in claims 13-19. None of the prior art disclosed or suggested to show the particular structure and/or the particular operation recited in these claims namely: the details of the power supply voltage detection unit (a load element, an NMOS transistor and an inverter) in combination with the details of the reset prevention unit (first - second pull-up means, a first pull-down means, a delay unit) and the power supply voltage level follower unit as recited in claim 18.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to My-Trang N. Ton whose telephone number is 571-272-1754. The examiner can normally be reached on 7:00 a.m - 5:30 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information

system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

My-Trang N. Ton
Primary Examiner
Art Unit 2816

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